Errata: CS5371/72 Errata

(Reference CS5371/72 data sheet revision DS255F2 dated SEP '05)

Erratum #1 : THD for ±5% tolerance unipolar power supplies.

Description:

Characterization testing shows degraded total harmonic distortion performance for certain unipolar power supply conditions.

Modulator does not meet -112 dB maximum THD performance for the following power supply configurations:

Test Conditions	VA+	VA-	VD	Max THD
-5% Unipolar Analog / -5% Digital	+4.75 V	0 V	+3.135 V	-100 dB
-5% Unipolar Analog / +5% Digital	+4.75 V	0 V	+5.25 V	-100 dB
+5% Unipolar Analog / -5% Digital	+5.25 V	0 V	+3.135 V	-100 dB
+5% Unipolar Analog / +5% Digital	+5.25 V	0 V	+5.25 V	-100 dB

Work Around:

Use of a unipolar analog power supply (VA+ = +5 V, VA- = GND) is not recommended.

If unipolar operation is required, use a nominal +5.00 V analog power supply to achieve data sheet performance.

Erratum #2 : CS5371 DC input glitch.

Description:

Applying a large DC input to the CS5371 device will sometimes show a 1 to 3 second glitch in the conversion output. This glitch is not present over short time scales or with a terminated input.

Work Around:

Connect pin 13 to GND. Power consumption will increase by 2x.

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Erratum #3 : CS5372 power down behavior.

Description:

Powering down only one channel of CS5372 can cause a 1 to 3 second glitch in the conversion output when measuring large DC signals. This glitch is not present over short time scales or with a terminated input.

Work Around:

Power down both PWDN1 and PWDN2 together when powering down the device.