

## Errata: CDB8416 Rev. B Evaluation Board

(Reference CDB8416 Data Sheet revision DS578DB2 dated Oct '02)

- In Software Mode, the PC control port implementation on the board does not allow for I<sup>2</sup>C read operation from the CS8406. CS8406 register read and write operation can be performed through SPI communication.
- The CS8416 external PLL component values on the board have changed from what is indicated in the schematic to reflect the current recommended component values. The correct values are:
  - ightharpoonup R59=3 k $\Omega$  (Rflt)
  - ➤ C57=22 nF (Cflt)
  - ➤ C58=1 nF (Crip)

## Contacting Cirrus Logic Support

For all product questions and inquiries contact a Cirrus Logic Sales Representative. To find one nearest you go to <a href="https://www.cirrus.com">www.cirrus.com</a>