

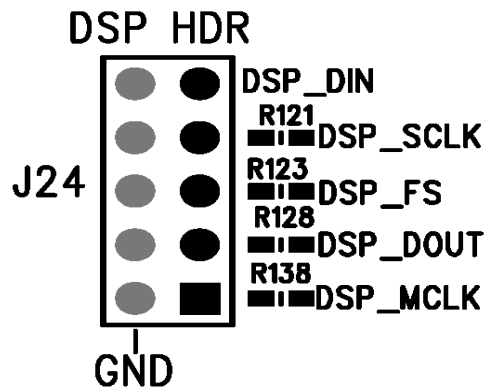
---

## Errata: CDB42438 Rev. B2

Reference CDB42438 Data Sheet

---

- Stake header J24 is labeled incorrectly. The correct label is shown below:



- Switch S1 and stake header J8 are labeled incorrectly as “USB”. The correct label is “MICRO”.
- Pin 1 on stake header J11 is labeled incorrectly as “SCK/CCLK”. The correct label is “SCL/CCLK”.
- The op-amp filters on the analog output channels are designed with 1 dB gain, delivering approximately 2.6 Vrms .

### Contacting Cirrus Logic Support

For all product questions and inquiries contact a Cirrus Logic Sales Representative.  
To find one nearest you go to [www.cirrus.com](http://www.cirrus.com)