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## **Errata: CS35L01/03 Rev. B0 Silicon**

(Reference CS35L01\_03\_DS909F1 Data Sheet)

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**WARNING:** While operating in either HD or FHD mode, connecting either or both of the OUT $\pm$  pins to VBATT, GND, or each other may result in permanent damage to the device.

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### **Contacting Cirrus Logic Support**

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.  
To find the one nearest you, go to <http://www.cirrus.com>